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Specification and Drawing, as originally filed, with Application for Patent Serial No:
CA 2314713, on July 28, 2000, by **CATENA NETWORKS CANADA INC.**, assignee
of Alberto Ginesi, François Tremblay and Scott McClennon, for "A Power Cutback
Strategy for G.DMT.BIS and G.LITE.BIS".

L. Régimbal
Agent certifié / Certifying Officer

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Date

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A Power Cutback Strategy for G.dmt.bis and G.lite.bis

Abstract

This invention relates to an extended politeness power cutback for both G.lite.bis and G.dmt.bis D/S and U/S signals, together with a G.hs.bis-defined maximum U/S and D/S transmit PSD levels. This latter is applied regardless the loop length and it is assumed to be based on MIB indications and on ATU-R and ATU-C characteristics. The rationale behind it is to both impose a general power consumption limitation and to limit interference into adjacent pairs (this being particularly important in DLC deployments). A G.hs.bis transaction (to be defined in future contributions) would exchange and negotiate the requirements coming from the operator (MIB-based) and from the ATUs. The extended politeness cutback can be applied on top of the max PSD limitation on short loops. For the ATU-C it ensures a statistical power savings over the served subscriber area without a significant data rate degradation. For U/S, it reduces the self-FEXT into adjacent pairs, thus improving the throughput for the modems served by the same feeder cable. This feature could be disabled by the operator when in presence of a NEXT-limited environment. The contribution specifically refers to G.dmt.bis; some changes are required for G.lite.bis when in presence of a Fast Retrain Power cutback.

1. Introduction:

It is well known that a power/PSD limitation of the transmit signal in G.lite.bis and G.dmt.bis (both of which are incorporated herein by reference) has several benefits (BA-065, BA-091, HC-028, BA-107, BA-046, MA-073) that can be summarized in mainly two points:

- 1) Lower power consumption in the transmitters (both ATU-C and ATU-R)
- 2) Lower cross-talks into adjacent pairs.

In a DLC environment this is particularly important for the D/S channel, as ADSL from the cabinet may be running in parallel with ADSL from the CO. In a self-FEXT limited environment, a loop length dependent power control for U/S does provide significant benefits as it improve and stabilize the performance across the subscriber base (MA-073).

On the other end, it has also been pointed out (BA-065) that in particular on short loops and/or for bit rates below 2 Mb/s (D/S) the aggregate power of G.dmt and G.lite is unnecessarily high.

Other relevant points are:

- 1) The reduced power and/or reduced PSD levels should apply not only to Showtime but also to Initialization (BA-107): this reflects an agreement reached during the ad-hoc meeting in Antwerp on power control. The rationale behind having a reduced power during Initialization is mainly for cross-talk limitation: a full power Initialization followed by a reduced power Showtime would create a time variant cross-talk environment thus forcing modems using pairs in the same binder group to re-train. The power control through the fine gains g_i , being applied only to Showtime, does not address this issue.
- 2) During the ad-hoc meeting in Antwerp it was also agreed that the selected power and PSD reduction shall be based on the characteristics of both the ATU-C and ATU-R devices and also on the characteristics imposed via the MIB.
- 3) Contribution BA-091 points out that a wider g_i 's value range would allow a more optimum use of the reduced power budget. This is true on short loops whenever some bins carry the max bit load with some margin. Some power can then be taken from this bins and re-allocated to bins with lower bit load thus recovering most of the data rate reduction imposed by the power cutback.
- 4) It has been agreed "That the inclusion of a mechanism for upstream power control to account for the effect of non-reciprocal FEXT shall be considered" (see point 15.1 in the G.dmt.bis issue list).

2. Proposed power cut back strategy

On the basis of the points made in the previous Section, the present invention provides:

- 1) The introduction of a mechanism to allow for a more significant power savings at the ATU-C without significant reductions in performance. To this end, the existing D/S politeness cutback is modified to allow for higher cutbacks at longer loops. The max amount of cutback is however kept to 12 dB as some implementations may have problems in applying higher cut-backs in the analog domain. A cutback applied in the digital domain would result in higher requirements for the dynamic range of the transmitter DAC.

- 2) The introduction of an U/S politeness cutback in order to limit the FEXT interference into adjacent pairs. This feature could be disabled by the operator.
- 3) An allowance for a max D/S PSD level and max U/S PSD level to be exchanged during G.H.S. These values should be used as the nominal for both Initialization and Showtime. On short loops, the proposed politeness cutback may or may not reduce the PSD levels further.

In order to implement the proposed points, it is necessary to modify part of the Transceiver Training of the Initialization (shown in Figure 1 below) in order to accommodate for a C-MSG-PCB to be transmitted from the ATU-C to the ATU-R (see Figure 2).

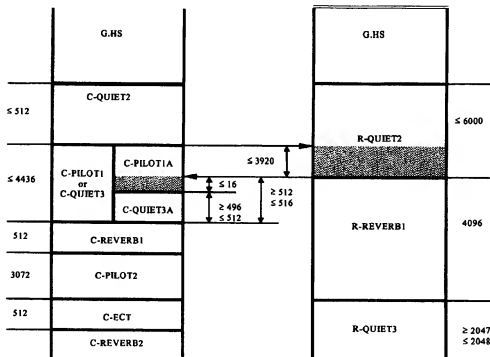


Figure 1 – Part of the current Transceiver Training phase of Initialization

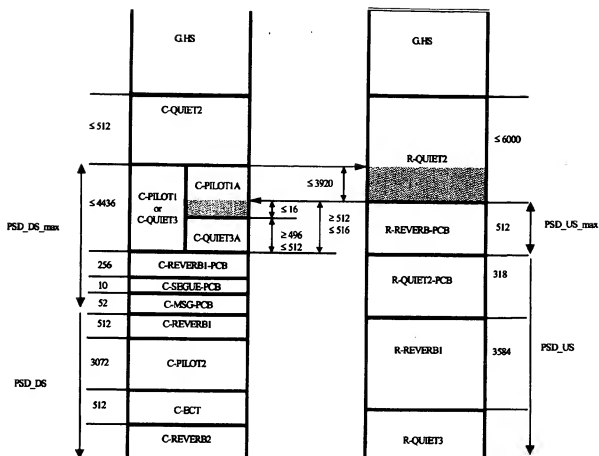


Figure 2 - Proposed changes to the Transceiver Training phase of Initialization

The modulation to be used for this message is the reliable 1 bit per DMT symbol modulation as in C-MSG1 and shall be transmitted at the PSD_DS_max level. The content of C-MSG-PCB is:

	Prefix	PCB_DS (dB)	PCB_US (dB)
Number of bits	32	5	5

Table 1- C-MSG-PCB message

The Prefix is a 4 byte prefix of {01010101 01010101 01010101 01010101₂}. Bits d4,d3,d2,d1,d0 of the field PCB_DS contain the power cut back in dB to be applied to the D/S signal starting from C-REVERB1:

Decimal value of {d4, d3,d2,d1,d0}	PCB_DS (dB)
0	0
1	2
...	...
31	62

Table 2 – D/S Power cut back (dB)

Bits u4,u3,u2,u1,u0 of the field PCB_US contain the power cut back in dB to be applied to the U/S signal starting from R-REVERB1:

Decimal value of {u4, u3,u2,u1,u0}	PCB_US (dB)
0	0
1	2
...	...
31	62

Table 3 – U/S Power cut back (dB)

3. Power cutback computation

The value of the Power cut backs to be applied would be computed at the ATU-C based on the following information:

- 1) the average loop attenuation in the U/S band (bin 8-17) as estimated during R-REVERB-PCB. We recommend to apply a politeness cutback for both U/S and D/S: for D/S we propose to re-partition the cutback over a longer loop coverage (approx. 6kft); for U/S we propose to introduce a politeness cut back based on a 1dB per 1dB loop attenuation basis (as proposed in MA-073) up to a max of 16 dB, in order to limit the FEXT interference into adjacent pairs (MA-072);
- 2) the max U/S and D/S PSD levels for the ATU-C and ATU-R as exchanged during G.hs.bis: PSD_US_max and PSD_DS_max.

The following tables describe how to compute the U/S and D/S politeness cutbacks from the estimated average U/S loop attenuation. This latter is defined as the difference between the U/S reference power in dBm and the total U/S power measured on subcarriers 7-18 during R-REVERB-PCB. The U/S reference power is defined as the total transmit power on subcarriers 7-18 using the R-REVERB-PCB transmit PSD level. This latter shall be the max U/S PSD level (PSD_US_max) as exchanged during G.HS.

Average U/S loop attenuation (dB)	>1 5	>1 4	>1 3	>1 2	>1 1	>1 0	>9	>8	>7	>6	>5	>4	>3	>2	>1	>0
D/S Politeness Power cut back (dB)	0	1	2	3	4	5	6	6	7	8	8	9	10	11	12	12

Average U/S loop attenuation (dB)	>1 6	>1 5	>1 4	>1 3	>1 2	>1 1	>1 0	>9	>8	>7	>6	>5	>4	>3	>2	>1	0
U/S Politeness Power cut back (dB)	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16

Table 4 – D/S and U/S politeness cutbacks as function of the estimated average U/S loop attenuation

The transmit PSD of C-REVERB1 would be the min of PSD_DS_max and $-40 - \text{D/S Politeness power cut back (dBm/Hz)}$. Therefore, the PCB_DS (dB) to be communicated through C-MSG-PCB would be: $\max \{0, \text{PSD_DS_max} - (-40 - \text{D/S Politeness Power cutback})\}$.

The transmit PSD of R-REVERB1 would be the min of PSD_US_max and $-38 - \text{U/S Politeness power cut back (dBm/Hz)}$. Therefore, the PCB_US (dB) to be communicated through C-MSG-PCB would be: $\max \{0, \text{PSD_US_max} - (-40 - \text{U/S Politeness Power cutback})\}$.

It is realized that the U/S politeness Power cut back would be beneficial only in a self-FEXT limited environment, so we propose to allow the operator to turn it off, if the environment in the feeder cable is NEXT-limited.

3. Extended fine gain range

In order to recover part of the capacity loss due to the cut-back it is recommended that in presence of a cut back with respect the nominal PSD values (-40 dBm/Hz for D/S and -38 dBm/Hz for U/S) higher than x dB the max value of the gi's range be extended to y (dB) $>2.5 \text{ dB}$. The values of x and y are for further studies.

4. Simulation results with the proposed politeness power cut-backs

The benefits of the U/S Politeness Power cut back are clearly presented in MA-073. Here, we evaluate the impact of the extended D/S politeness Power cut-back on performance. The following Table presents the performance degradation vs. loop length (26 AWG) for 24HDSL NEXT and 24 DSL NEXT with -140 dBm/Hz AWGN. The set of bins 36-255 is assumed to be active.

Loop Length (kft)	% of Data rate reduction with 24 DSL NEXT	% of Data rate reduction with 24 HDSL NEXT
0	0	0
1	0	1.5
2	0	2.7
3	0	2.2
4	0	2.4
5	1.5	3.2
6	2.1	3

Table 5 – Performance degradation due to the proposed D/S politeness power cutback

As shown, there is only a negligible performance degradation with the proposed politeness cutback. Other Cross-talk combination have not been simulated for a lack of time. However, similar results are expected.

5. References

- BA-065 "G.gen: G.dmt and G.lite transmitting with reduced total aggregate power, or with reduced PSD", Alcatel, Antwerp, Belgium, June 19-23, 2000
- BA-091 "G.gen:Enabling efficient use of power limited transmitter for G.lite.bis and G.dmt.bis", Nortel Networks, Antwerp, Belgium, June 19-23, 2000
- HC-028 "G.gen: Implementation Suggestions for Initials Transmit Power in the Handshake", Sigma Tel, Huntsville, Canada, July 31 – August 4, 2000
- BA-107 "Report of an Ad-Hoc meeting on "G.dmt and G.lite working permanently with reduced (aggregate) power and PSD", Alcatel, Antwerp, Belgium, June 19-23, 2000
- BA-046 "G.gen: Reporting on the Initial Transmit Power in the Handshake", Sigma Tel., Antwerp, Belgium, June 19-23, 2000
- MA-073 "G.gen.bis: Short Loop Upstream Power Control in FEXT-limited ADSL Systems", Nortel Networks, Melbourne, Australia March 29 – April 2, 1999.
- MA-072 "G.gen.bis: An Improved Upstream FEXT Model for FEXT-limited ADSL Systems ", Nortel Networks, Melbourne, Australia March 29 – April 2, 1999.

6. Summary

1. The D/S politeness power cutback should be extended to longer loops as per table 4.
2. An U/S politeness Power cutback shall be defined to limit FEXT into adjacent pairs. The amount of cut back should be computed at the ATU-C according to table 4.
3. The amount of U/S and D/S politeness power cutback shall be communicated from the ATU-C to the ATU-R by using the messaging scheme depicted in figure 2 and described in Section 2.
4. The first part of Initialization, prior to the estimation of the politeness power cutbacks, shall be done at the max PSD levels as exchanged/negotiated during G.hs.bis.
5. In presence of a cut back with respect the nominal PSD values (-40 dBm/Hz for D/S and -38 dBm/Hz for U/S) higher than x dB the max value of the gi's range shall be extended to y (dB) >2.5 dB.

LOOP- AND RATE-DEPENDENT DS POWER CUTBACK FOR POWER SAVINGS IN A XTU-C DSL TRANSCIVER

(Filed on July 28, 2000 as Canadian Application Serial Number _____)

Background of Invention:

The challenge solved with this invention is the reduction of xDSL transceiver power requirements on subscriber loops with excess SNR margin for a given required link capacity.

ADSL ATU-C transmitters reduce their transmit power on very short loops to avoid overloading the remote ATU-R receiver. This "politeness cutback" is determined according to an estimate of loop loss in the upstream signal path and applies on loops shorter than ~2-3kft 26AWG-equivalent. Other DS transmitter power cutbacks are also specified in the splitterless ADSL draft standard, for the purpose of reducing the DS signal level at the remote end in the presence of an off-hook telephone set. There is no agreed mechanism to implement DS power cutback for other purposes; specifically, there is no mechanism defined for power cutback, to reduce the ATU-C power requirements, where there is excess SNR margin at the ATU-R receiver.

The benefit of such a cutback capability can be significant in Digital Loop Carrier applications, for example, where subscriber loops are typically shorter than seen at CO-resident line interfaces and where power and thermal budgets are also very tight. Assuming a DMT transmit signal with peak-to-average-ratio (PAR) of 14.5dB¹, DS power cutback can yield significant savings on line driver AC power alone, with fixed driver supply rails. As illustrated below, these savings can exceed 500mW for a class ? driver with 13dB of cutback.

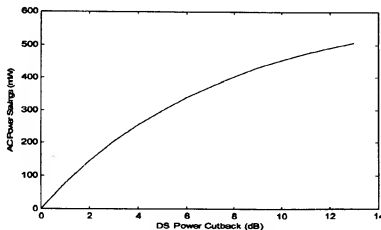


Figure 1: Fixed Supply Line Driver Power Savings vs. DS Power Cutback

¹ PAR reduction techniques claiming 3-5dB reductions exist in the literature. In practice, however, these techniques lose their effectiveness when the signal passes through analog or digital filters in the transmit path.

Figures 2-4 illustrate the attainable DS rates² over loop lengths of 0-12kft (26AWG), with DS power cutback to 15dB in the presence of crosstalk from other DSLs on adjacent pairs. The crosstalk scenario for the three figures is 49 other G.lite (splitterless ADSL) disturbers, 10 HDSL disturbers or 5 adjacent binder T1 disturbers, respectively.

The DS Politeness Cutback (of 0-12dB) is not shown here (will apply at loop lengths up to ~3kft (26AWG) after which politeness cutback would be 0).

The case of Figure 2 would be more typical of a residential neighbourhood, where T1 or HDSL services in the same or adjacent binders would be less common. It should be pointed out that if all local subscribers were being served off power-cutback-capable DLCs, the G.lite crosstalk levels would actually be lower due to the 'extra' power cutback and the rates would be improved over those shown in figure 2.

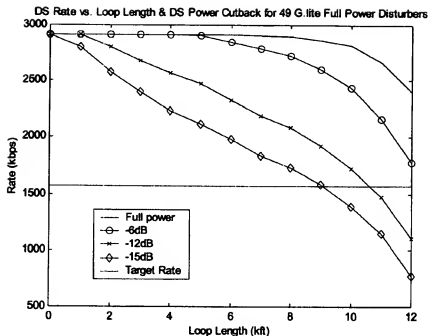
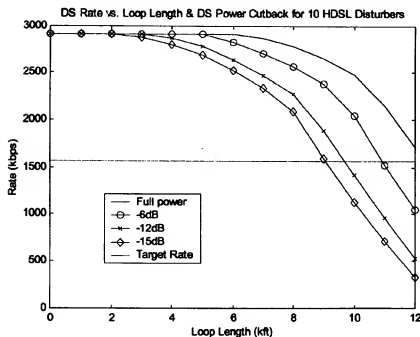
With 10 HDSL disturbers (figure 3), full (1.5Mbps) capacity is possible with 15dB of cutback to loop lengths of 9kft (26AWG) – within CSA loop engineering rules.

With T1 disturbers in adjacent binders (figure 4), the DS power reduction - on a loop of length 12kft - may be restricted to ~6dB to avoid significant capacity hits.

² The attainable rates assume:

- 4dB SNR margin
- 3dB coding gain
- DS carriers 36-127
- DS receiver noise floor of -136.8dBm/Hz

Where a higher (than 4dB) SNR margin is mandated (by the operator) and/or the DS receiver noise floor exceeds -136.8dBm/Hz (required to meet test cases with DS carriers 36-127) the attainable rate or allowable DS power cutback for a given rate will be reduced.

**Figure 2 DS Rates with 49 G.lite Disturbers****Figure 3 DS Rates with 10 HDSL Disturbers**

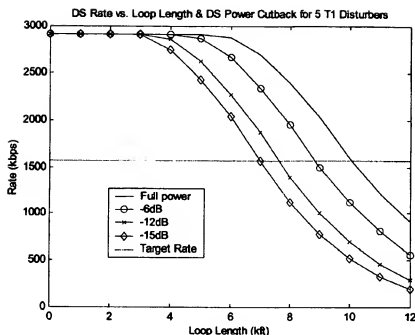


Figure 4 DS Rates with 5 Adj. Binder T1 Disturbors

This invention describes a DS power cutback mechanism that reduces DS transmit power based on a metric of excess SNR margin at the DS receiver. A variety of techniques are described, some of which can be implemented with ATU-Rs compliant to the current draft G.lite standard (ITU-T G.992.2) and some which may be more optimal (faster to initialize and/or maximizing achievable cutback) but require changes to the existing draft standard.

Description of invention

Determination and setting of DS power cutback – where excess SNR margin is available at the DS receiver – is not directly supported in the G.992.x initialization procedure. It can be implemented in a non-standard fashion, for interoperability with G.992.2 ATU-Rs but a more efficient mechanism can be proposed for G.992.2bis.

Non-standardized DS Power Reduction Initialization Procedures

The amount of DS transmit power reduction that can be tolerated for a given capacity target will be a function of the loop and the crosstalk environment which together determine the SNR per receiver sub-carrier. Unfortunately, this information is not

available to the ATU-C. It may be inferred from the Bi's and Gi's sent in R-B&G, near the end of initialization, but it is too late to implement a DS power cutback without impacting the ATU-R, which will need to adapt its receive AGC at a minimum.

In the absence of changes to G.992.2 to address this, there are a variety of approaches to initialization that could permit power savings without significant capacity losses. NOTE: In all cases, the amount of cutback vs. the measured upstream power could be an operator-specified parameter.

A) Reduce DS power based on measured US power alone — PREFERRED

US power is measured at the ATU-C in order to determine the DS power politeness cutback level already.

The politeness cutback is used on very short loops only (~2-3kft 26AWG equivalent), so there is no impact on DS capacity regardless of the crosstalk noise environment at the ATU-R.

It is clearly possible to cutback DS power more aggressively, based on the results shown in figures 1-3, although the degree to which this is done would depend on the willingness to risk a failure during full-initialization. For example, a cutback of 12dB may be employed where the loop length is estimated to be less than 9kft 26AWG-equivalent. Assuming that the loop length – and equivalent DS loop losses – can be adequately estimated from the US power measured at the ATU-C AND that there are no adjacent binder T1 disturbers, a 12dB cutback should not significantly reduce the DS capacity below ~1.5Mbps.

Unexpected capacity losses may still occur if the ATU-R is not capable of performance meeting the standard test cases because of a noisy front end or if it is not robust against AM ingress noise. In those cases, the cutback could be reduced or eliminated and a second full-initialization triggered.

B) Two passes through full-initialization during initial install or whenever on-hook loop conditions have changed significantly

In this case, the ATU-C forces a second full-initialization once it has received the Bi's, Gi's, performance (SNR) margin and attainable rate from the ATU-R near the end of the first initialization procedure. It can estimate the impact of a cutback from those parameters (if correctly reported) and determine how much cutback can be tolerated. A second initialization procedure would then be repeated with the ATU-C transmitting at its 'new' level.

This process would require twice as long (up to ~20seconds) to initialize the link but would only need to be invoked whenever a change has occurred in the noise environment at the ATU-R (for example, when a HDSL service is newly deployed).

C) Reduce DS power by excess margin reported by ATU-R

Negotiation of rates on the link occurs over a series of handshakes between the ATU-C and the ATU-R. The ATU-C starts by providing the ATU-R with a list of 4 rate options as well as the required SNR margin to be met. The ATU-R responds to those options indicating the highest rate, if any, that can be supported. It also provides the ATU-C with the average DS loop attenuation and the SNR margin at that rate (should be approximately same at all carriers if the Gi's have been calculated to equalize the SNR margin across all carriers). If the ATU-R is able to support the highest rate, with a SNR margin in excess of the desired margin, then a power reduction of the amount of the difference can be implemented. The difficulty with this approach is that the ATU-R will not necessarily be designed to adapt to a signal level change at this point in the initialization – forcing a new full initialization

D) Over-specify margin in first round of rate negotiations

In the first round of rate negotiations, the ATU-C specifies a minimum SNR margin with an additional 'N' dB of margin. If the ATU-R responds its first reply that it can support a high link rate at that inflated margin – the ATU-C drops its Tx power by 'N' dB and sets the required margin lower by an equivalent amount for the second round of rate negotiations. If the ATU-R cannot support the target link rate with the inflated (by 'N') margin, then the ATU-C does not cutback its transmit power, but still reduces the minimum required SNR margin (by 'N') for the second round of negotiations. This approach suffers the same issue with ATU-R AGC adaptation as in method C above. It also requires that the ATU-R indicate whether it can support any of the options in the first round; the G.992.x standard currently allows the ATU-R to postpone a decision, which would make this approach untenable.

E) Count on ATU-R to signal attainable cutback implicitly in DS Gi's

Finally, it is also possible – but not required – that the ATU-R reduce the Gi's to be used on each of the downstream carriers where there is (otherwise) excess margin. If so, this would – in effect – set the downstream power cutback desired here. As noted, one cannot assume that any vendor's ATU-R will specify the Gi's in that manner (i.e. they may use only to equalize the margin on each carrier, while keeping an overall unnecessarily large SNR margin). In addition, the Gi's are communicated to the ATU-C late in the initialization process. As a result, any change in transmit gain must be exact as there is no

time for the ATU-R to adapt its receiver to a imprecise gain change, before the start of SHOWTIME. In practice, this forces the signal to be reduced in the digital domain, before the DAC, and so can place excess demands on the DAC dynamic range.

Proposal for DS Power Reduction in G.992.2bis

The amount of DS power reduction that can be tolerated for a given rate and SNR margin requires knowledge of the loop loss crosstalk noise spectral density at each carrier. It is also desirable that the DS power reduction be implemented at the ATU-C in time for the ATU-R to adapt its receiver before SHOWTIME.

The basic objective is to manage the signal power being launched into one end of the loop, so as to provide a signal level, at the receiver end, sufficient to meet the targeted capacity but not higher. In T1.413/G.992.1 or G.992.2, the upstream transmitter cutback may be determined and applied, during link initialization, as follows:

1. The ATU-R measures the DS received³ noise level (during C-QUIET2)
 - any receiver AGC should be set to maximum gain here to support a noise level measurement of sufficient resolution
2. The ATU-R measures the DS received power without cutback (during C-REVERB1) and estimates the average loop attenuation.
3. The ATU-R calculates the amount of DS cutback possible without reducing estimated upstream capacity by more than 2(?)%. The capacity calculations are based on received signal and receiver input referenced noise levels as measured at each upstream carrier frequency and mapped to an equivalent per-carrier SNR_i . Note that this capacity formulation is based on the measured signal and noise levels, independent of near end echo, timing jitter or mis-equalization effects. An additional 6dB of margin is included in the SNR gap figure used in the capacity calculation to cover these impairments as well as measurement tolerances and to provide a hedge against the introduction of more NEXT sources into the same binder group.

The line rate capacity estimate, less the FEC overhead, (in bits per symbol period) with no cutback is given by

$$C = \sum_i b_i; \quad b_i = \text{round}(\log_2(1 + \frac{SNR_i}{\Gamma})) \quad \text{and } 2 \leq b_i \leq b_{\max}$$

where b_{\max} is the maximum # of bits/carrier supported in the ATU-R receiver⁴,

³ A minimum noise floor measurement resolution capability may be specified to ensure that ATU-Rs are capable of supporting a specific DS power cutback for a given DS loop loss and crosstalk noise environment.

⁴ Note that the ATU-C transmitter may have a lower value of b_{\max} but that is not known at the ATU-R prior to reception of C-MSG S1, though it could be specified for exchange during G.hs.

and

$$\Gamma = 10^{(9.8+10-3)/10}$$

is the SNR gap for a bit-error-rate of 10^{-7} with 10dB margin and 3dB coding gain, for G.992.2.

The maximum *cutback* (in linear form – corresponding to some multiple of 2dB) is determined such that the capacity with that cutback, C' , is greater than or equal to $0.98 \times C$, where

$$C' = \sum_i b'_i; \quad b'_i = \text{round}(\log_2(1 + \frac{SNR_i / \text{cutback}}{\Gamma})) \quad \text{and} \quad 2 \leq b_i \leq b_{\max}$$

4. The ATU-R encodes the required cutback into a field of R-MSG1, signaling the correct downstream power cutback to the ATU-C. Allowing for a power cutback range of 0-15dB, in 1dB steps, a 4 bit field is required in R-MSG1.
 5. After receiving and decoding the message from the ATU-R, the ATU-C applies the power cutback – 500-3500 symbols into C-MEDLEY. The ATU-R is able to adapt its receiver gain, to the new ATU-C transmit level, over this interval prior to completion of C-MEDLEY.
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Claims

1. An xDSL xTU-C transceiver capable of determining excess SNR margin (at normal transmit levels) on the downstream (DS) link and reducing DS transmit signal power, for a given target DS capacity.
 2. A method for estimating excess SNR margin, based on an estimate of loop loss derived from measured received power in the upstream band and a assumed DS receiver input-referenced noise spectral power density.
 3. A method for estimating achievable DS power cutback – for a given target DS capacity - based on the reported SNR margin and selected Bi's (bits/carrier 'i') and Gi's (gain per carrier 'i') from the DS receiver – in a multi-carrier system.
 4. A method for estimating achievable cutback, based on first over-specifying the required DS SNR margin by an amount equal to the desired cutback, then reducing the DS transmit power for any link that could have supported the target DS capacity
-

with that excess margin.

5. A method for determining excess SNR margin, for a given target capacity, at the DS receiver early in link initialization and communicating the achievable and required DS power cutback to the DS transmitter in time to implement the cutback and allow the DS receiver to retrain its receive gain before beginning data transmission.
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Back-up Material

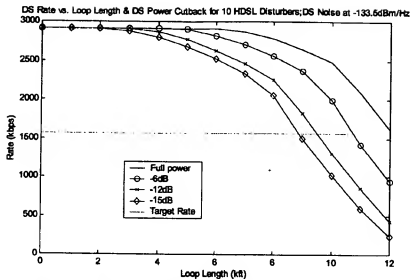


Figure x: With DS noise floor at -133.5dBm/Hz (vs. -136.8)

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